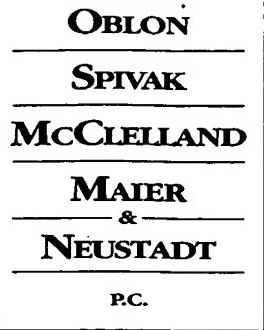




Docket No.: 219660US2S
Date Allowed: December 2, 2003



COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

ATTORNEYS AT LAW

ECKHARD H. KUESTERS
(703) 413-3000
EKUESTERS@OBLON.COM

RE: Application Serial No.: 10/075,465

Applicants: Hisato OYAMATSU

Filing Date: February 15, 2002

For: SEMICONDUCTOR DEVICE HAVING PATTERNED
SOI STRUCTURE AND METHOD FOR
FABRICATING THE SAME

Group Art Unit: 2829

Examiner: GEYER, SCOTT B.

SIR:

Attached hereto for filing are the following papers:

COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Eckhard H. Kuesters

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219660US-2S



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :

HISATO OYAMATSU : EXAMINER: GEYER, SCOTT B.

SERIAL NO: 10/075,465 : DATE ALLOWED: December 2, 2003

FILED: February 15, 2002 : GROUP ART UNIT: 2829

FOR: SEMICONDUCTOR DEVICE
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STRUCTURE AND METHOD
FOR FABRICATING THE SAME

COMMENTS ON EXAMINER'S STATEMENT
OF REASONS FOR ALLOWANCE

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

The Examiner's Statement of Reasons for allowance at page 3 of the notice of allowability ambiguously refers to figures 4, 10, 11, and 13, stating "... the prior art ... does not teach or render obvious ... the instant invention regarding a semiconductor device having first, second and third semiconductor regions characterized by the height differences as recited in claim 1 and as exemplified by applicant's figures 4, 10, 11 and 13." Since the drawings may show non-claimed features, Applicants' state on the record that the claims are not limited by unclaimed features noted in the drawings.

U.S. SERIAL NO. 10/075,465

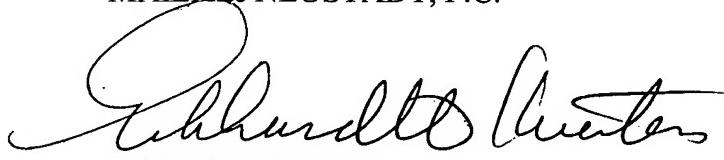
Date Allowed: December 2, 2003

Comments on Examiner's Statement of Reasons Filed: December 18, 2003

Accordingly, a revised statement of reasons for allowance is recommended and respectfully requested.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.



Eckhard H. Kuesters
Attorney of Record
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